

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kiran V. Chatty et al.

Application No.: 10/709,041

Confirmation No.: 3040

Filed: April 8, 2004

Art Unit: 2815

For: LOW TRIGGER VOLTAGE ESD NMOSFET
TRIPLE-Well CMOS DEVICES

Examiner: M. E. Warren

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT (37 CFR 1.121)

MS Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Notice of Non-Compliant Amendment under 37 C.F.R. § 1.121 mailed on September 27, 2007 in connection with the above-identified U.S. patent application, Applicants respond as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.